

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application Number	Not yet assigned
Filing Date	December 12, 2003
First Named Inventor:	Kumar et al.
Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	42P17964

Sheet	1	of	2
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

/Michael Moore Jr/

Date Considered
11/28/2007

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Sheet	2	of	2	Attorney Docket Number	42P17964
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
/MM/	1.	INTEL, www.intel.com/design/network/products/npfamily/ixp2800.htm , "Intel IXP2800 Network Processor", October 10, 2003.			
/MM/	2.	SHREEDHAR, M. et al., "Efficient Fair Queuing Using Deficit Round Robin", Washington University in St. Louis, November 18, 1994, pages 1-26.			
/MM/	3.	INTEL, "OC-192 DRR Egress Scheduler", Intel IXA Software Building Blocks Developer's Manual, pages 305-317.			
/MM/	4.	"Weighted Random Early Detection on the Cisco 12000 Series Router", pages 1-6.			

Examiner Signature	/Michael Moore Jr/	Date Considered	11/28/2007
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